

Amendment Dated February 17, 2006

Reply to Office Action of January 20, 2006

**Amendments to the Claims:** This listing of claims will replace all prior versions, and listings, of claims in the application

Listing of Claims:

1. (Currently amended) A protection system for a coin cell comprising spaced, substantially parallel and substantially co-extensive top and bottom conductive surfaces said system comprising:

a first insulating wafer adapted to fit over said top conductive surface of said coin cell said first wafer comprising a first opening;

a second insulating wafer adapted to fit over a portion of said bottom conducting surface of said coin cell, said second wafer comprising an insulating area extending fully under the first opening and a second opening located so that when the two wafers are placed over the coin cell conductive surfaces, the first opening does not overlap the second opening;

and

wherein said coin cell is designed to fit in a cell holder having upper and lower contact electrodes designed to contact said top and bottom conductive surfaces respectively in non overlapping areas and wherein said openings in said top and bottom wafers are positioned so that when said insulating ~~member is~~ wafers are placed over a coin cell adapted to fit said coin cell holder, said upper and lower electrodes contact said top and bottom conducting surfaces respectively through said wafer openings when said battery is inserted in a first orientation in said holder and contact the insulating wafer when said coin cell is inserted in said holder in a second, inverted orientation.

2. (Original) The protection system of claim 1 further comprising a tab connecting said first wafer and said second wafer.

3. (Cancelled).

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4. (Original) The protection system of claim 1 wherein said first and said second wafers are generally circular each comprising a center and having dimensions commensurate with said coin cell top and bottom conducting surfaces respectively, and wherein said first wafer opening encompasses said first wafer center and second wafer comprises two openings positioned symmetrically around said second wafer center outside of said second wafer center.

5. (Original) The protection system of claim 2 wherein said tab is longer than a thickness of said coin cell whereby when said top and said bottom wafers are placed on said top and said bottom coin cell conducting surfaces said tab extends from one side of said coin cell at least 1/8 inches.

6. (Cancelled).

7. (Currently Amended) A method for preventing applying a reverse polarity voltage to an electronic circuit from a coin cell used to power said circuit the ,method comprising:

providing a first insulating wafer over a top conductive surface of said coin cell said first wafer comprising a first opening;

providing a second insulating wafer over a portion of a bottom conducting surface of said coin cell, said second wafer comprising an insulating area extending fully under the first opening and a second opening located so that when the two wafers are placed over the coin cell conductive surfaces, the first opening does not overlap the second opening;

and

wherein said coin cell is designed to fit in a cell holder having upper and lower contact electrodes designed to contact said top and bottom conductive surfaces respectively in non overlapping areas and wherein said openings in said top and bottom wafers are positioned so that when said insulating ~~member is~~ are placed over a

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coin cell adapted to fit said coin cell holder said upper and lower electrodes contact said top and bottom conducting surfaces respectively through said wafer openings when said battery is inserted in a first orientation in said holder, and contact the insulating wafer when said coin cell is inserted in said holder in a second, inverted orientation.

8. (Currently amended) A coin cell comprising a top conductive surface representing an electrical contact of a first polarity and a substantially parallel bottom conductive surface representing an electrical contact of a second polarity, said top conductive surface being substantially parallel to said top conductive surface, and

a first insulating wafer on said top conductive surface covering substantially all of said top conductive surface ~~said first insulating wafer~~ said first insulating wafer comprising a first opening exposing a portion of said top conductive surface and

a second insulating wafer on said bottom conducting surface completely covering at least an area on said bottom conducting surface opposite said first opening on said top conductive surface whereby exposed areas in said top and said bottom conductive surfaces do not overlap.

9. (Original) The coin cell of claim 8 wherein said second insulating wafer covers substantially all of said second conducting surface said second insulating wafer also comprising an second opening exposing a portion of said bottom conducting surface; said first opening does not overlap said second opening.

10. (Original) The coin cell according to claim 9 wherein said top surface is circular and has a center and said first opening is also circular and is located substantially in the center of the top conductive surface, and wherein said bottom surface also comprises a center and the second wafer opening is an off center arcuate opening.

11. (Original) The coin cell battery according to claim 8 wherein said top surface includes a raised contact and said raised contact extends through said first wafer opening.

12. (Original) The coin cell according to claim 8 wherein said first and said second insulating wafers are interconnected with a connecting tab.

13. (Original) The coin cell according to claim 12 wherein said tab extends along a side of said coin cell.

14. (Original) The coin cell according to claim 8 wherein said first and said second wafers are removably attached to said coin cell.

15. (Original) The coin cell according to claim 8 wherein said coin cell is designed to fit in a cell holder having upper and lower contact electrodes designed to contact said top and bottom conductive surfaces respectively and wherein said openings in said top and bottom wafers are positioned so that when said cell is inserted in a first orientation in said holder said upper and lower electrodes contact said top and bottom conducting surfaces through said first and second wafer openings respectively and contact the insulating wafer when said coin cell is inserted in said holder in a second, inverted orientation.

16. (Original) The coin cell according to claim 11 wherein said top conductive surface is substantially planar and said raised contact is attached to said planar top surface substantially on said top surface center.

17. (Original) The coin cell according to claim 9 wherein said second wafer comprises two arcuate openings located symmetrically around said bottom surface center.